# **2.5 Context Switching:**

In embedded systems, context switching refers to the process of saving and restoring the state of a task or process so that the system can switch between multiple tasks or processes. Embedded systems often have limited resources, including processing power and memory, so efficient context switching is crucial to ensure the system's responsiveness and overall performance. Here are some key considerations for context switching in embedded systems:

### **Task States:**

Tasks in embedded systems typically have different states, such as running, ready, blocked, or suspended.

The context switching mechanism needs to save the state of a task when it is preempted or when it voluntarily gives up the CPU, and later restore that state when the task is scheduled to run again.

#### **Interrupts:**

Interrupts are a common mechanism in embedded systems to handle events and asynchronous input.

Context switching often occurs in response to interrupts, where the current task's state is saved, and the interrupt service routine (ISR) is executed.

It's important to minimize the time spent in the ISR to reduce the impact on system latency.

#### **Scheduler Design:**

The scheduler is responsible for determining which task should run next.

Priority-based schedulers are common in embedded systems, where tasks with higher priority are given precedence.

The scheduler needs to efficiently manage the task queue and make decisions based on task priorities and other scheduling policies.

#### **Stack Management:**

Each task in an embedded system typically has its own stack to store local variables and function call information.

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During a context switch, the context-switching code must switch to the stack of the next task and save/restore the stack pointer accordingly.

## **Memory Footprint:**

Embedded systems often have constraints on memory usage.

The context-switching mechanism should be designed to have a minimal memory footprint to conserve resources.

## **Real-time Considerations:**

Many embedded systems have real-time requirements, meaning that tasks must meet certain deadlines.

Context switching should be deterministic and not introduce unpredictable delays that could violate real-time constraints.

## **Power Consumption:**

In battery-powered embedded systems, minimizing power consumption is crucial.

Context switching should be designed to be as energy-efficient as possible.

## Hardware Support:

Some embedded systems may have hardware support for context switching, such as a contextswitching unit or features like multiple processor cores.

Utilizing hardware support can improve the efficiency of context switching.

In summary, context switching in embedded systems requires careful consideration of factors such as task states, interrupts, scheduler design, stack management, memory usage, real-time requirements, power consumption, and potential hardware support. Efficient context switching is essential for achieving optimal performance in resource-constrained embedded environments.