## **Hartley oscillators**

Hartley Oscillator is a type of harmonic oscillator which was invented by Ralph Hartley in 1915. These are the Tuned Circuit Oscillators which are used to produce the waves in the range of radio frequency and hence are also referred to as RF Oscillators. Its frequency of oscillation is decided by its tank circuit which has a capacitor connected in parallel with the two serially connected inductors, asshown by Figure 1.

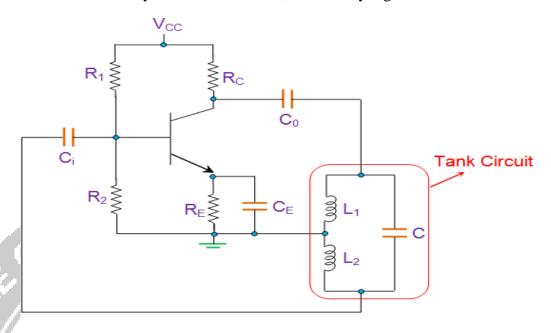


Figure 1 Hartley Oscillator

- Note that in the collector-tuned circuit, two inductor coils are used.
- > One end of these coils is grounded.
- > If we make the tickler coil an integral part of the circuit, we get Hartley Oscillator.
- When the tank circuit resonates, the circulating current flows through  $L_1$  in series with  $L_2$ . Hence the equivalent inductance is
- > O/p current is collector current h<sub>fe</sub>I<sub>b</sub>
- $\blacktriangleright$  h<sub>ie</sub> is the input impedance of the transistor, output of the feedback is current I<sub>b</sub> which is input of transistor .
- Now change current source to voltage source
- $V_0 = h_{fe} I_b X_{L2} = h_{fe} I_b jw L_2 \dots (1)$
- ightharpoonup L<sub>1</sub> and h<sub>ie</sub> parallel

$$I = \frac{1}{[X_{L2} + X_C] + [X_{L1} | | h_{ie}]} - \cdots (2)$$

$$E CPT | M | ZE CPT | M |$$

Imaginary part of R.H.S must be zero

$$W^{3} h_{fe} L_{1} L_{2} C [h_{ie} - w^{2} h_{ie} C (L_{1} + L_{2})] = 0$$

$$w = \frac{1}{\sqrt{C(L_{1} + L_{2})}}$$

- $\triangleright$  Here the  $R_C$  is the collector resistor while the emitter resistor  $R_E$  forms the stabilizing network. Further the resistors  $R_1$  and  $R_2$  form the voltage divider bias network for the transistor in common-emitter CE configuration.
- Next, the capacitors C<sub>i</sub> and C<sub>o</sub> are the input and output decoupling capacitors while the emitter capacitor C<sub>E</sub> is the bypass capacitor used to bypass the amplified AC signals. All these components are identical to those present in the case of a common-emitter amplifier which is biased using a voltage divider network.
- However, Figure 1 also shows one more set of components viz., the inductors  $L_1$  and  $L_2$  and the capacitor C which form the tank circuit (shown in red enclosure).
- On switching ON the power supply, the transistor starts to conduct, leading to an increase in the collector current, I<sub>C</sub> which charges the capacitor C. On acquiring the maximum charge feasible, C starts to discharge via the inductors L<sub>1</sub> and L<sub>2</sub>.
- This charging and discharging cycles result in the damped oscillations in the tank circuit. The oscillation current in the tank circuit produces an AC voltage across the inductors L<sub>1</sub> and L<sub>2</sub> which are out of phase by 180° as their point of contact is grounded. Further from the figure, it is evident that the output of the amplifier is applied across the inductor L<sub>1</sub> while the feedback voltage drawn across L<sub>2</sub> is applied to the base of the transistor.
- Thus one can conclude that the output of the amplifier is in- phase with the tank circuit's voltage and supplies back the energy lost by it while the energy fed back to amplifier circuit will be out-of-phase by 180°.
- The feedback voltage which is already 180° out-of-phase with the transistor is provided by an additional 180° phase-shift due to the transistor action.
- Hence the signal which appears at the transistor's output will be amplified and will have a net phase-shift of 360°.

At this state, if one makes the gain of the circuit to be slightly greater than the feedback ratio given by

$$eta = rac{L_1}{L_2}; \; if \, the \, coils \, are \, wound \, on \, different \, cores$$

$$\beta = \frac{L_1 + M}{L_2 + M}$$

(if the coils are wound on the same core with M indicating the mutual inductance)

- Then the circuit generates the oscillations which can be sustained by maintaining the gain of the circuit to be equal to that of the feedback ratio. This causes the circuit in Figure 1 to act as an oscillator as it would then satisfy both the conditions of the Barkhausen criteria.
- > The frequency of such an oscillator is given as

$$F=rac{1}{2\pi\sqrt{L_{eff}C}}$$

- ➤ Hartley oscillators are available in many different configurations including series-or shunt-fed, common-emitter or common-base configured, and BJT (Bipolar Junction Transistor) or FET (Field Effect Transistor) amplifier based. Further it is to be noted that the transistor-based amplifier section of Figure 1 can even be replaced by an amplifier of any other kind like that of an inverting amplifier formed by an Op-Amp as shown by Figure 2.
- The working of this kind of oscillator is similar to that of the one shown earlier. However, here, the gain of the oscillator can be individually adjusted using the feedback resistor  $R_f$  due to the fact that the gain of the inverting amplifier is given as  $-R_f/R_1$ .
- From this, it can be noted that, in this case, the gain of the circuit is less dependent on the circuit elements of the tank circuit.
- This increases the stability of the oscillator in terms of its frequency.

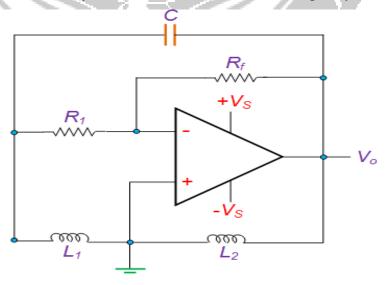


Figure 2 Hartley Oscillator Using an Op-Amp

- ➤ Hartley Oscillators are advantageous as they are easy-tunable circuits with a very few components including a capacitor and either two inductors or a tapped coil.
- ➤ This results in a constant amplitude output throughout its wide operational frequency range which typically ranges from 20 KHz to 30 MHz.

- ➤ However, this kind of oscillator is not suitable for low frequency as it would result in a large-sized inductor which makes the circuit bulky.
- Further, the output of Hartley Oscillator has high content of harmonics in it and hence does not suit for the applications which require pure sine wave.

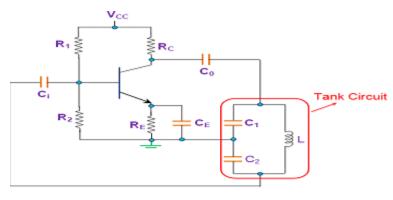
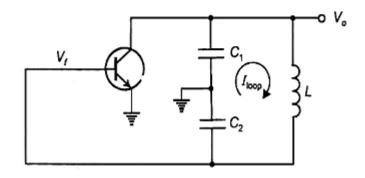


Figure Colpitts Oscillator



## **Colpitts oscillators**

Colpitts Oscillator is a type of LC oscillator which falls under the category of Harmonic Oscillator and was invented by Edwin Colpitts in 1918.

Figure above shows a typical Colpitts oscillator with a tank circuit in which an inductor Lis connected in parallel to the serial combination of capacitors  $C_1$  and  $C_2$  (shown by the red enclosure).

- ➤ Widely used in commercial signal generators.
- > Uses two capacitors instead of the inductive voltage divider.

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$$C = \frac{C_1 C_2}{C_1 + C_2}$$

$$\beta = \frac{1/\omega C_2}{1/\omega C_1} = \frac{C_1}{C_2}$$

- ➤ Other components in the circuit are the same as that found in the case of common-emitterCE which is biased using a voltage divider network i.e. R<sub>C</sub> is the collector resistor, R<sub>E</sub> is the emitter resistor which is used to stabilize the circuit and the resistors R<sub>1</sub> and R<sub>2</sub> form the voltage divider bias network.
  - Further, the capacitors  $C_i$  and  $C_o$  are the input and output decoupling capacitors while the emitter capacitor  $C_E$  is the bypass capacitor used to bypass the amplified AC signals.

Here, as the power supply is switched ON, the transistor starts to conduct, increasing the collector current  $I_C$  due to which the capacitors  $C_1$  and  $C_2$  get charged. On acquiring the maximum charge feasible, they start to discharge via the inductor L. During this process, the electrostatic energy stored in the capacitor gets converted into magnetic flux which in turn is stored within the inductor in the form of electromagnetic energy. Next, the inductor starts to discharge which charges the capacitors once again. Likewise, the cycle continues which gives rise to the oscillations in the tank circuit.

Further the figure shows that the output of the amplifier appears across  $C_1$  and thus is inphase with the tank circuit's voltage and makes-up for the energy lost by re-supplying it. On the other hand, the voltage feedback to the transistor is the one obtained across the capacitor  $C_2$ , which means the feedback signal is out-of-phase with the voltage at the transistor by  $180^{\circ}$ . This is due to the fact that the voltages developed across the capacitors  $C_1$  and  $C_2$  are opposite in polarity as the point where they join is grounded. Further, this signal is provided with an additional phase-shift of  $180^{\circ}$  by the transistor which results in a net phase-shift of  $360^{\circ}$  around the loop, satisfying the phase- shift criterion of Barkhausen principle.

At this state, the circuit can effectively act as an oscillator producing sustained oscillations by carefully monitoring the feedback ratio given by  $(C_1/C_2)$ . The frequency of such a Colpitts Oscillator depends on the components in its tank circuit and is given by

O/p current is collector current h <sub>fe</sub> I <sub>b</sub>
h <sub>ie</sub> is the input impedance of the transistor. output of the feedback is current I <sub>b</sub> which is input of
transistor.
Now change current source to voltage source
$V_0 = h_{fe} I_b X_{C2} = h_{fe} I_b (1/jwC_2)(1)$

$$I = \frac{-V_{0}}{[X_{C2} + X_{L}] + [X_{C1} | I | h_{ie}]} ----- (2)$$

$$X_{C2} + X_{L} = jwL + \frac{1}{jwC_{2}} X_{C1} | I | h_{ie} = \frac{(1/jwC_{1}) h_{ie}}{((1/jwC_{1}) + h_{ie})}$$

$$I_{b} = I X \frac{XC_{1}}{XC_{1} + h_{ie}} ----- (4)$$

Imaginary part of R.H.S must be zero

$$w h_{ie} [C_1 + C_2 - w^2 L C_1 C_2] = 0$$

$$w = \frac{1}{\sqrt{L(C_1C_2 / C_1 + C_2)}}$$

$$F = rac{1}{2\pi\sqrt{LC_{eff}}}$$

Where, the C<sub>eff</sub> is the effective capacitance of the capacitors expressed as  $C_1 + C_2$ As a result, these oscillators can be tuned either by varying their inductance or the

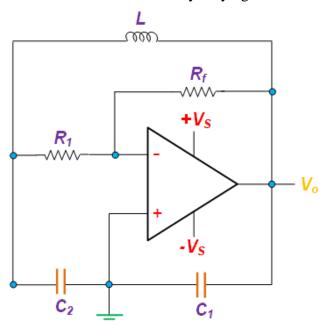


Figure Colpitts Oscillator Using an Op-Amp

capacitance. However the variation of L does not yield a smooth variation. Hence they are usually tuned by varying the capacitances which are generally ganged, due to which a change in any one of them changes both of them. Nevertheless, the process is tedious and requires special large- valued capacitor. Thus, the Colpitts oscillators are seldom preferred in the applications where in the frequency varies but are more popular as fixed frequency oscillators due to their simple design. Further they offer better stability in comparison with the Hartley Oscillators as they are exempted from the mutual inductance effect present in-between the two inductors of the latter case.

Apart from the BJT-based Colpitts Oscillator shown, they are also realizable using valves or FET (Field Effect Transistor) or Op-Amp. Figure 2 shows such a Colpitts oscillator which uses an Op-Amp in inverting configuration in its amplifier section while the tank circuit remains similar to that in the case of Figure 1. This kind of circuit functions almost analogous to that of the one explained earlier. However, here the gain of the oscillator can be adjusted individually just by using the feedback resistor  $R_f$ , as the gain of the inverting amplifier is given as  $-R_f / R_1$ . From this, it can be noted that, in this case, the gain of the circuit is less dependent on the circuit elements of the tank circuit.

Typically, the operating frequency of the Colpitts oscillators ranges from 20 KHz to 300 MHz. However they can even be used for microwave applications as their capacitors provide low reactance path for the high-frequency signals. This results in better frequency stability as well as a better sinusoidal output waveform. Moreover, they are also extensively used as surface acoustical wave (SAW) resonators, sensors and in mobile and communication systems.

