## QUANTIZATION DUE TO TRUNCATION AND ROUNDIND,QUANTIZATION NOISE

## Quantization:

*Discuss the various methods of quantization.
*Derive the expression for rounding and truncation errors

* Discuss in detail about Quantization error that occurs due to finite word length of registers.

The common methods of quantization are

1. Truncation
2. Rounding
3. Truncation

- The abrupt termination of given number having a large string of bits (or)
- Truncation is a process of discarding all bits less significant than the LSB that is retained.
- Suppose if we truncate the following binary number from 8 bits to 4 bits, we obtain
- 0.00110011 to 0.0011
( 8 bits) (4 bits)
- $\quad 1.01001001$ to 1.0100
(8 bits)
(4 bits)
- When we truncate the number, the signal value is approximated by the highest quantization level that is not greater than the signal.

2. Rounding (or) Round off

- Rounding is the process of reducing the size of a binary number tofqhnelagyd size of 'b' bits such that the rounded b -bit number is closest to the original unquantised number.


## Error Due to truncation and rounding:

- While storing (or) computation on a number we face registers length problems. Hence given number is quantized to truncation (or) round off.
i.e. Number of bits in the original number is reduced register length.


## Truncation error in sign magnitude form:

- Consider a 5 bit number which has value of

$$
0.11001_{2} \quad \rightarrow(0.7815)_{10}
$$

- This 5 bit number is truncated to a 4 bit number
$0.1100_{2}$
$\rightarrow(0.75)_{10}$
i.e. 5 bit number $\rightarrow 0.11001$ has ' 1 ' bits

4 bit number $\rightarrow 0.1100$ has ' $b$ ' bits

- Truncation error, $\begin{array}{rll}\mathrm{e}_{\mathrm{t}} & = & 0.1100-0.11001 \\ & = & -0.00001 \xrightarrow{\rightarrow}(-0.03125)_{10}\end{array}$
- Here original length is ' l ' bits. ( $l=5$ ). The truncated length is ' $b$ ' bits.
- The truncation error, $\mathrm{e}_{\mathrm{t}}$

| $=$ | $2^{-\mathrm{b}}-2^{-1}$ |
| :--- | :--- |
| $=$ | $-\left(2^{-1}-2^{-\mathrm{b}}\right)$ |
| $=$ | $-\left(2^{-5}-2^{-4}\right)$ |

- The truncation error for a positive number is

$$
-\left(2^{-b}-2^{-l}\right) \leq e_{t} \leq 0 \quad \rightarrow \text { Non causal }
$$

- The truncation error for a negative number is
$0 \leq e_{t} \leq\left(2^{-b}-2^{-l}\right)$
$\rightarrow$ Causal


## Truncation error in two's complement:

- For a positive number, the truncation results in a smaller number and hence remains same as in the case of sign magnitude form.
- For a negative number, the truncation produces negative error in two's complement

$$
-\left(2^{-b}-2^{-l}\right) \leq e_{t} \leq\left(2^{-b}-2^{-l}\right)
$$

## Round off error (Error due to rounding):

- Let us consider a number with original length as ' 5 ' bits and round off length as ' 4 ' bits.

$$
0.11001 \xrightarrow{\text { Round of to }} 0.1101
$$

- Now error due to rounding $e_{r}=\frac{2^{-b}-2^{-1}}{2}$

Where $\quad \mathrm{b} \rightarrow$ Number of bits to the right of binary point after rounding

$$
L \rightarrow \text { Number of bits to the right of binary point before rounding }
$$

- Rounding off error for positive Number:

$$
-\frac{2^{-b}-2^{-l}}{2} \leq e_{r} \leq 0
$$

- Rounding off error for negative Number:

$$
0 \leq e_{r} \leq \frac{2^{-b}-2^{-r}}{2}
$$

- For two's complement

$$
-\frac{2^{-b}-2^{-l}}{2} \leq e_{r} \leq \frac{2^{-b}-2^{-l}}{2}
$$

***************************************************************************************

## Quantization Noise:

*Derive the expression for signal to quantization noise ratio
*What is called Quantization Noise? Derive the expression for quantization noise power.

## A/D converter



- The analog signal is converted into digital signal by ADC
- At first, the signal $x(t)$ is sampled at regular intervals $t=n T$, where $n=0,1,2 \ldots$ to create sequence $x(n)$. This is done by a sampler.
- Then the numeric equivalent of each sample $x(n)$ is expressed by a finite number of bits giving the sequence $x_{q}(n)$
- The difference signal $e(n)=x_{q}(n)-x(n)$ is called quantization noise (or) A/D conversion noise.
- Let us assume a sinusoidal signal varying between $+1 \&-1$ having a dynamic range 2
- ADC employs (b+1) bits including sign bit. In this case, the number of levels available for quantizing $x(n)$ is $2^{b+1}$.
- The interval between the successive levels is
$q=\frac{2}{2^{b+1}}=2^{-b}$
Where $\mathrm{q} \quad \rightarrow \quad$ quantization step size
If $\mathrm{b}=3$ bits, then $\mathrm{q}=2^{-3}=0.125$

